

31059 U.S. PTO  
10/013089

12/07/01

# U.S. UTILITY Patent Application

PATENT NUMBER and  
ISSUE DATE

APPL NUM 10013089	FILING DATE 12/07/2001	CLASS 257	SUBCLASS 720 <sup>+</sup>	GAU 2813	EXAMINER <i>Dolan</i>
<b>**APPLICANTS:</b> Pan Jeng-Yang; Chen Hung-Fa;					
<b>**CONTINUING DATA VERIFIED:</b>					
<b>BEST AVAILABLE COPY</b>					
<b>**FOREIGN APPLICATIONS VERIFIED:</b>					
PG-PUR <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		35 U.S.C. 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO 67,200-627	
Verify and Acknowledged Examiners's initials					
TITLE : Semiconductor wafer tray positioning					
U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)					

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>Assistant Examiner</b>	<b>CLAIMS ALLOWED</b>	
			Total Claims	Print Claim for O.G.
<b>ISSUE FEE</b>		<b>Primary Examiner</b>	<b>DRAWING</b>	
Amount Due	Date Paid		Sheets Drwg.	Figs. Drwg.
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>PREPARED FOR ISSUE</b>	<b>Application Examiner</b>	
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